

FIG.1

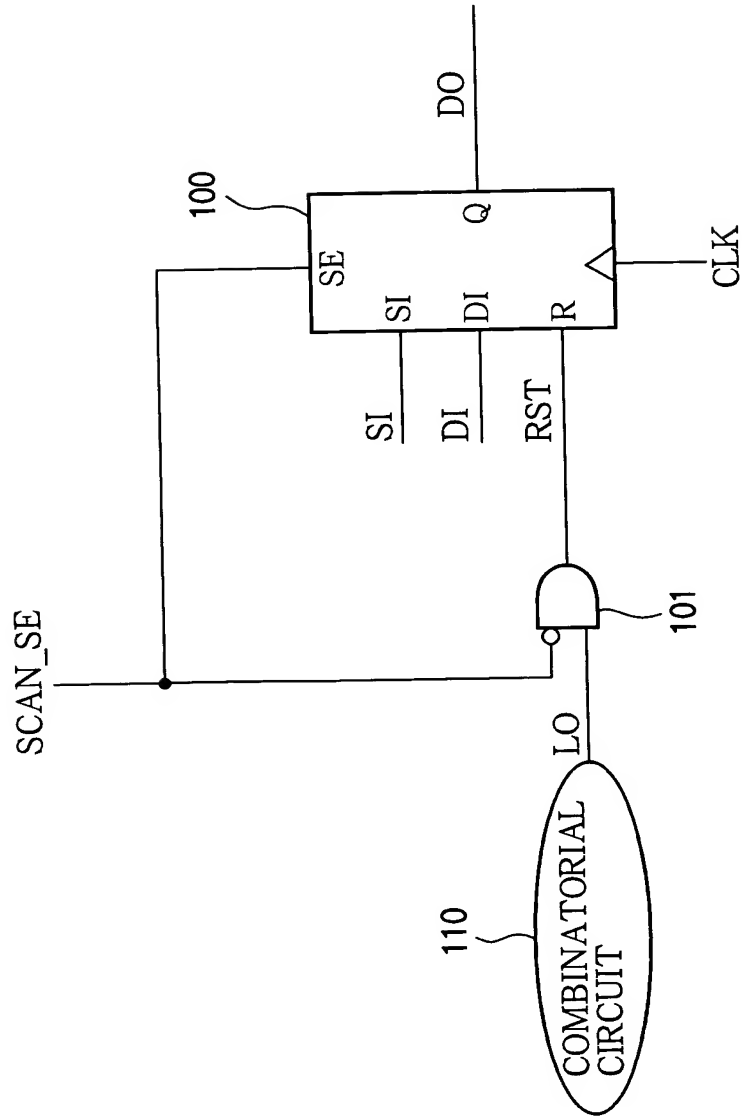


FIG.2

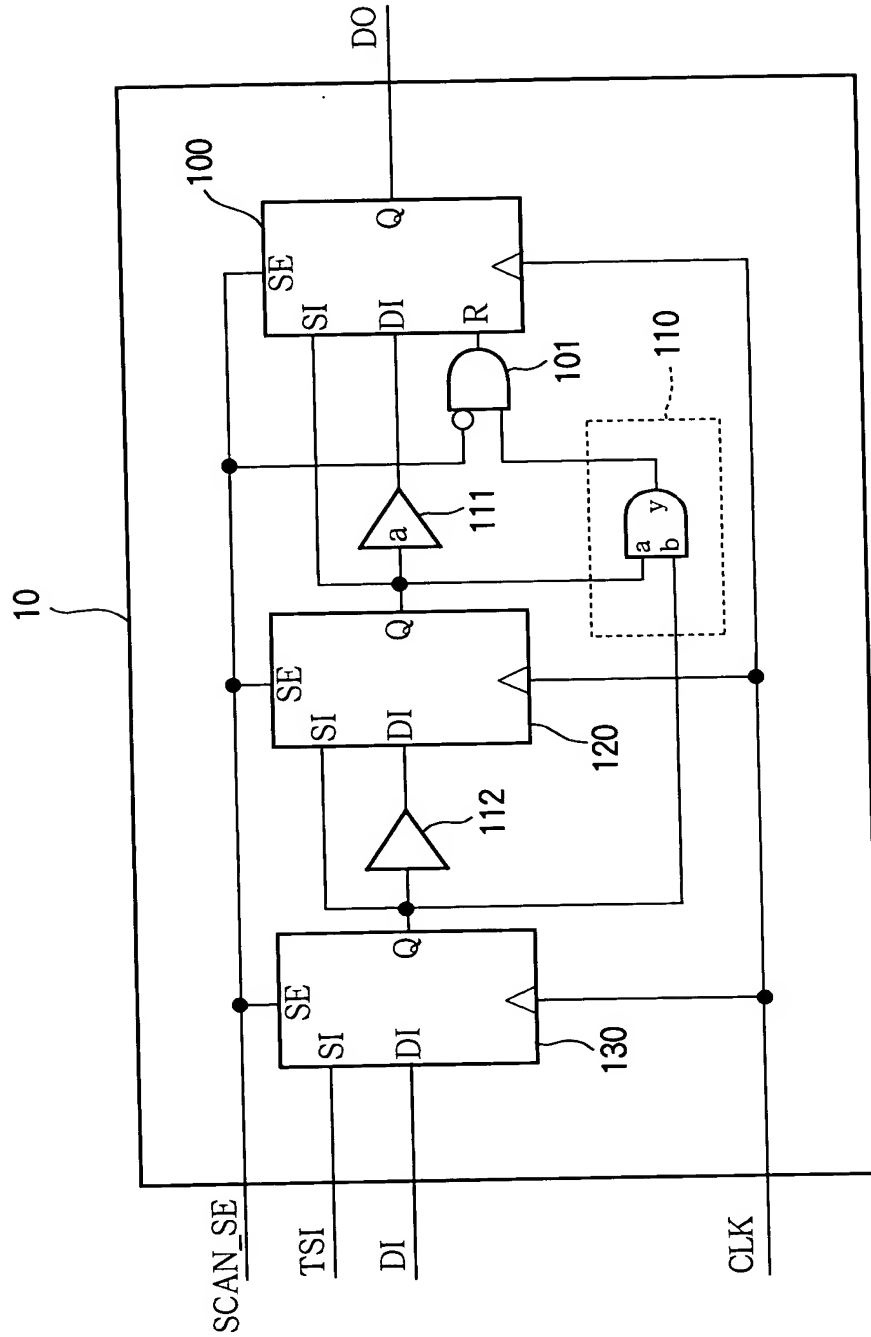


FIG.3

a	b	y	RST	FLIP-FLOP 100	SA0 FAULT AT PIN a OF BUFFER 111
0	0	0	0	NOT RESET	NOT TESTABLE
0	1	0	0	NOT RESET	NOT TESTABLE
1	0	0	0	NOT RESET	TESTABLE
1	1	1	1	RESET	NOT TESTABLE (BECAUSE RESET)



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FIG.5

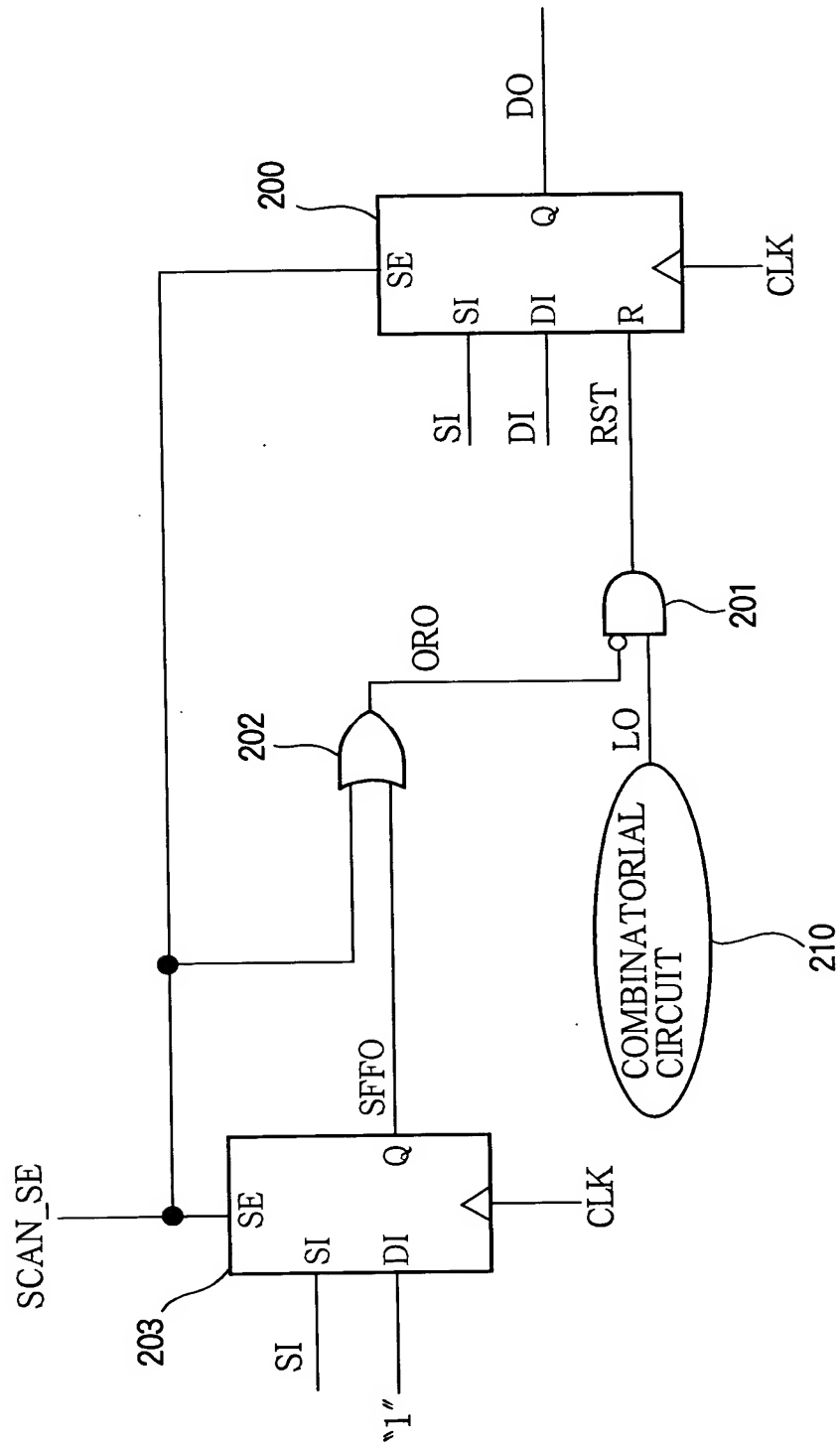


FIG. 6

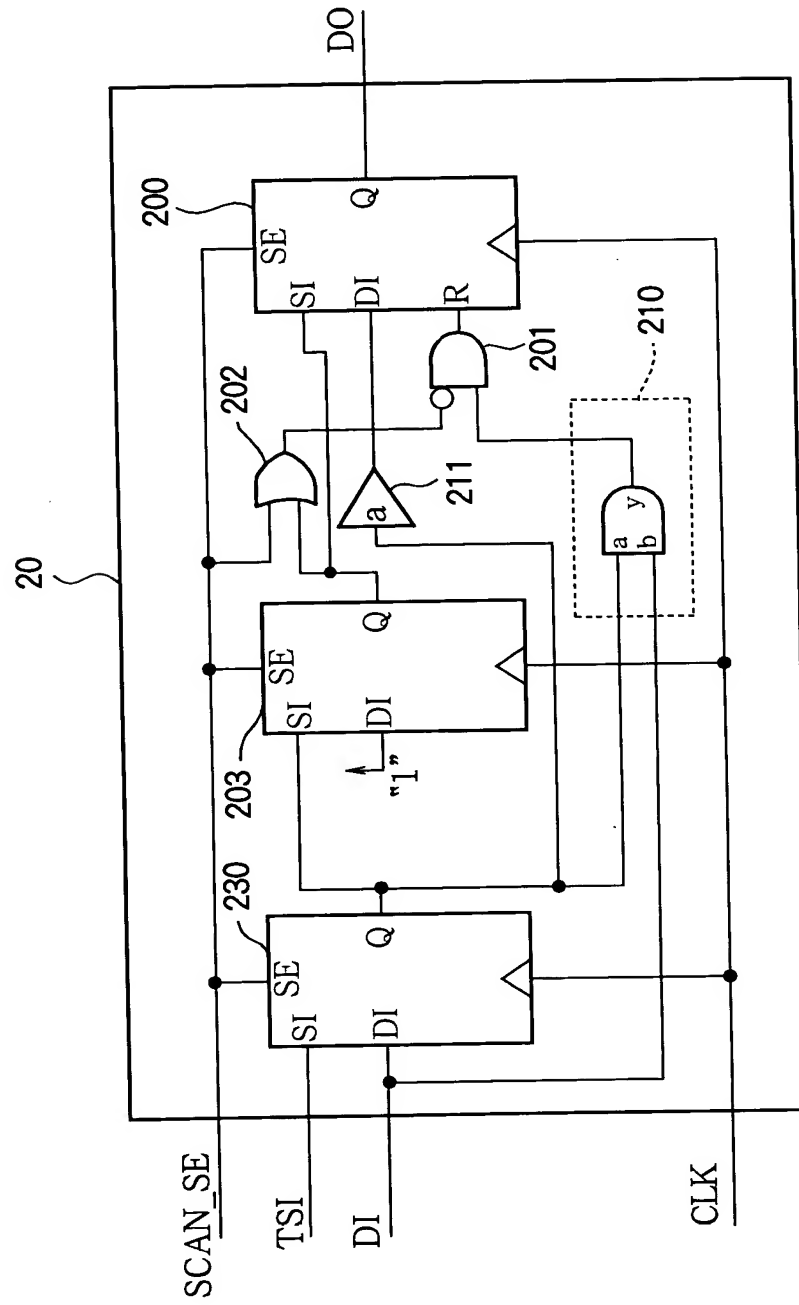


FIG.7

a	b	y	ORO	RST	FLIP-FLOP 200 OR 300	SA0 FAULT AT PIN a OF BUFFER 111
0	0	0	0	0	NOT RESET	NOT TESTABLE
0	1	0	0	0	NOT RESET	NOT TESTABLE
1	0	0	0	0	NOT RESET	TESTABLE
1	1	1	0	1	RESET	NOT TESTABLE (BECAUSE RESET)
0	0	0	1	0	NOT RESET	NOT TESTABLE
0	1	0	1	0	NOT RESET	NOT TESTABLE
1	0	0	1	0	NOT RESET	TESTABLE
1	1	1	1	0	NOT RESET	TESTABLE

FIG.8

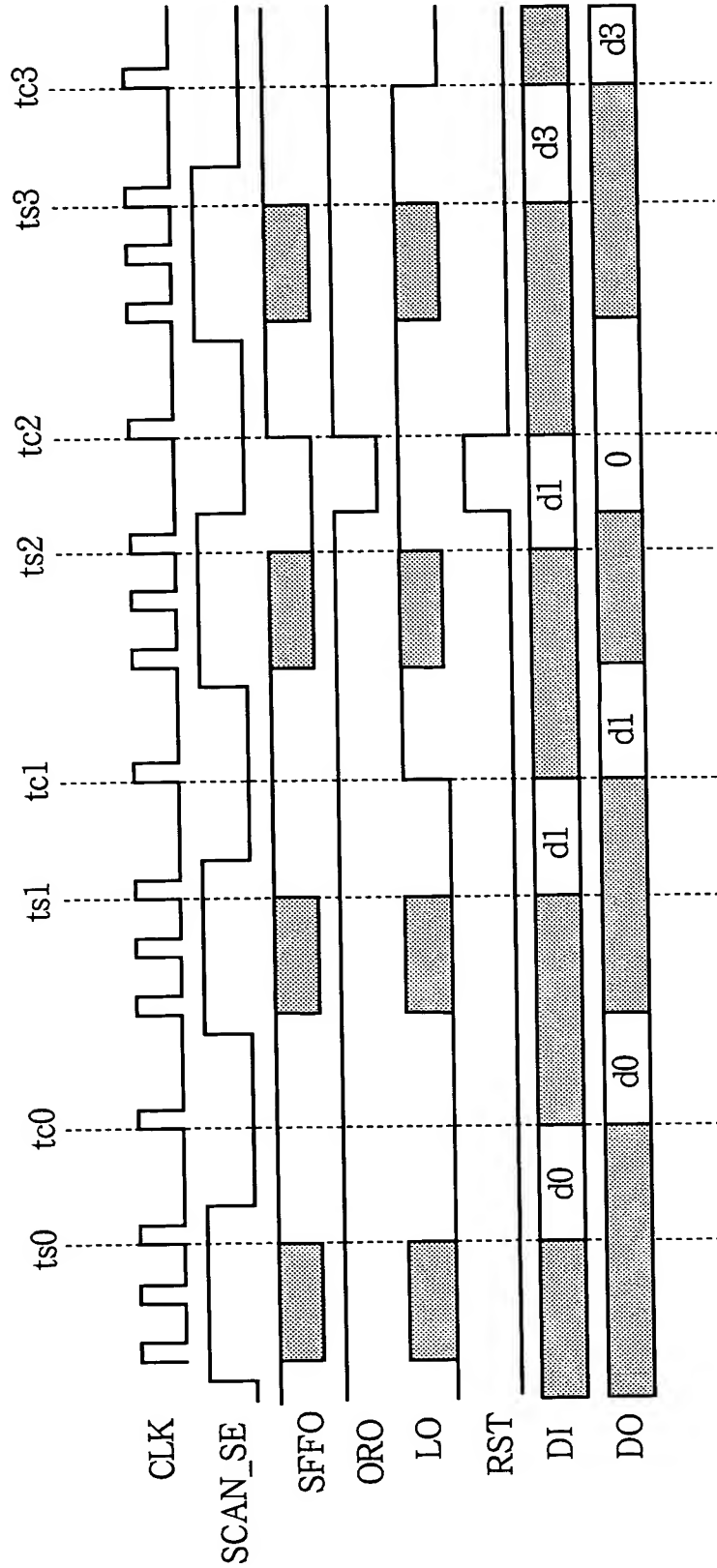




FIG. 9

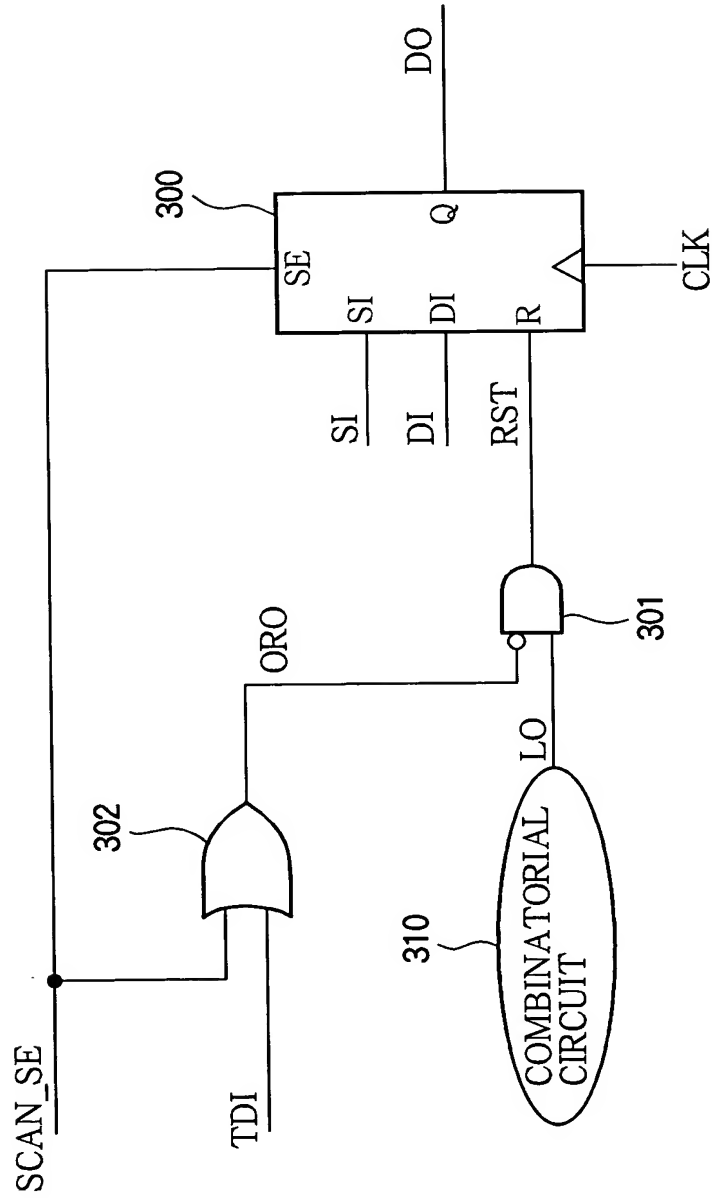
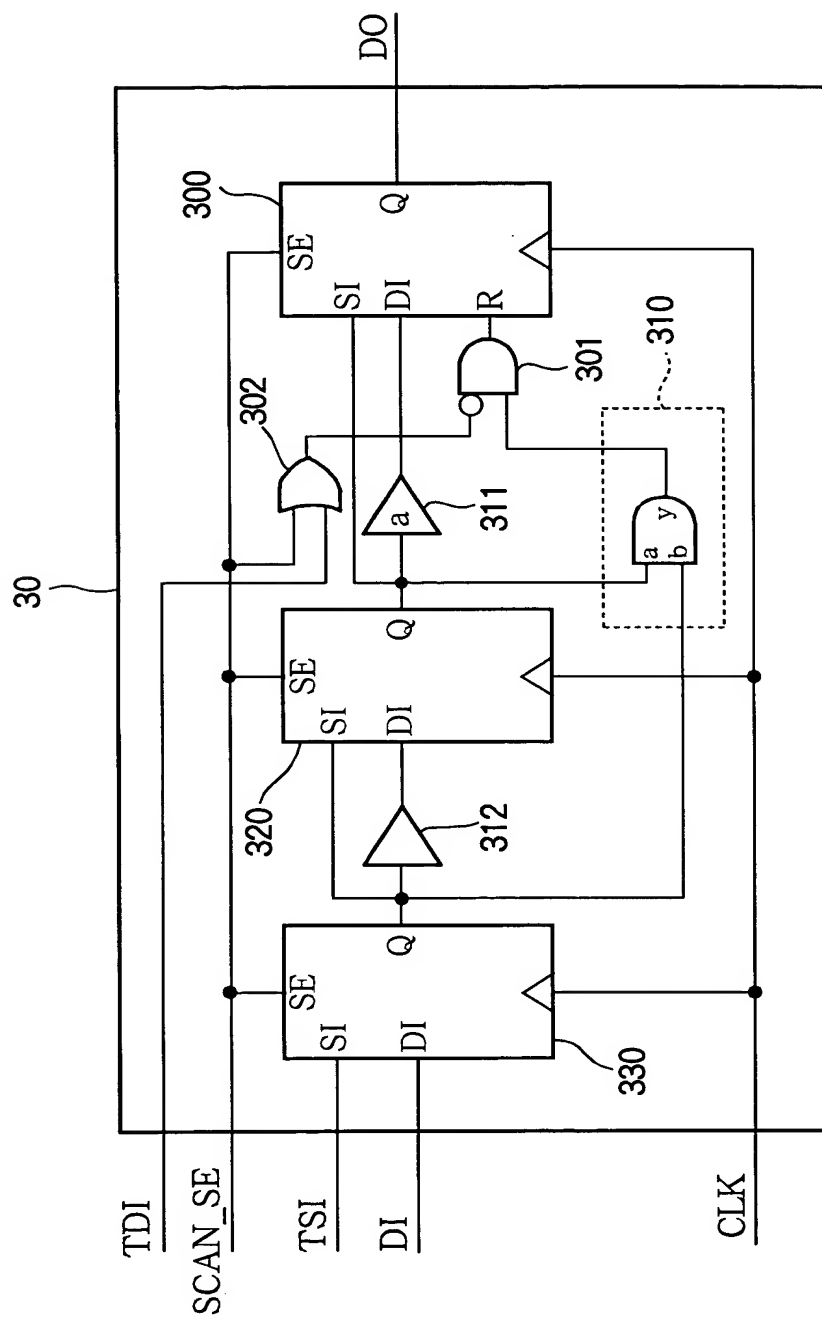


FIG.10



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FIG.11

